

REMARKS

This preliminary amendment is presented to place the application in proper form for examination and to eliminate multiple dependency from the present claims. No new matter has been added. Early examination and favorable consideration of the above-identified application is earnestly solicited.

Attached hereto is a mark-up version of the changes made to the specification and claims by the current amendment. The attached page is captioned "Version with markings to show changes made".

Any additional fees or charges required at this time in connection with the application may be charged to our Patent and Trademark Office Deposit Account No. 03-2412.

Respectfully submitted,

COHEN, PONTANI, LIEBERMAN & PAVANE

By



Michael C. Stuart

Reg. No. 35,698

551 Fifth Avenue, Suite 1210

New York, N.Y. 10176

(212) 687-2770

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VERSION WITH MARKINGS TO SHOW CHANGES MADE

3. A synchroniser as claimed in claim 1 [or 2], wherein said providing means, said controlling means and said estimating means are in the digital domain.

5. A synchroniser as claimed in [any preceding] claim 1, wherein said providing means comprises a digital corrector.

6. A synchroniser as claimed in [any preceding] claim 1, wherein a rough correction is provided by said control signal.

8. A synchroniser as claimed in claim 6 [or 7], wherein a finer correction is provided.

10. A synchroniser as claimed in [any preceding] claim 1, wherein said estimator is arranged to determine that the difference between two successive levels has increased if a difference between the upper of said levels and [a] an estimated level for an actual signal provides a signal at a higher level than a signal provided by a difference between a lower of said levels and an estimated level for the actual signal.

11. A synchroniser as claimed in [any preceding] claim 2, wherein said estimator is arranged to determine that the difference between two successive levels has increased if a

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difference between the upper of said levels and [a] an estimated level for an actual signal provides a signal at a higher level than a signal provided by a difference between a lower of said levels and an estimated level for the actual signal.

12. A synchroniser as claimed in [any preceding] claim 1, wherein said estimator is arranged to determine that an actual signal has changed if a differences between the upper of said levels and an actual signal provides a signal at substantially the same level as a signal provided by a difference between a lower of said levels and the actual signal, said same level being different to a previous level for said actual signal.

13. A synchroniser as claimed in [any preceding] claim 1, wherein said synchroniser is arranged to at least one of acquire [and/or] and track frequency error.

14. A synchroniser as claimed in [any preceding] claim 1, wherein said synchroniser is arranged to at least one of acquire [and/or] and track timing error.

15. A receiver comprising a synchroniser as claimed in [any preceding] claim 1.